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Processor resource distributor and method Title **Publication Date** 2001/06/07 Certification Number 136368 1998/09/04 Application Date Application No. 087114683 IPC G06F-013/00 Inventor BAKER-HARVEY, MICHE US: KAWAGUCHI, ATSUO JP Applicant EQUATOR TECHNOLOGIES, INC. US **Priority Number** 1997/09/04 US 19970923173 Abstract A device and method for distributing time available on a processor among two or more alternative tasks or threads and scheduling their

among two or more alternative tasks or threads and scheduling their execution. Each of the tasks specifies multiple levels of time usage on the process under which it can operate, including a best quality of service which uses the most time and a minimal quality of service which uses the least time. The invented system and method quarantees that each task will always be able to run at least its minimal quality of service and includes methods for obtaining optimal quality of service from all of the tasks.